

2.1.F Atmega-Peripherie/Interrupts – Ergänzungen und Bilder

2.1.F.1 Interrupt-Quellen bei verschiedenen ATMEL-Controllern

Nr.	Name	Bemerkung	Name in m8def.inc
1	RESET	Reset-Eingang, Einschalten	—
2	INT0	Externer Interrupt 0	INT0addr
3	INT1	Externer Interrupt 1	INT1addr
4	TIMER2_COMP	Timer 2	OC2addr
5	TIMER2_OVF	Timer 2	OVF2addr
6	TIMER1_CAPT	Timer 1	ICP1addr
7	TIMER1_COMPA	Timer 1	OC1Aaddr
8	TIMER1_COMPB	Timer 1	OC1Baddr
9	TIMER1_OVF	Timer 1	OVF1addr
10	TIMER0_OVF	Timer 0	OVF0addr
11	SPI_STC	SPI-Schnittstelle	SPIaddr
12	USART_RX	RS232-Schnittstelle	URXCaddr
13	USART_UDRE	RS232-Schnittstelle	UDREaddr
14	USART_TX	RS232-Schnittstelle	UTXCaddr
15	ADC	A/D-Umsetzer	ADCCaddr
16	EE_RDY	EEPROM	ERDYaddr
17	ANA_COMP	Komparator	ACIaddr
18	TWI	I ² C-Bus	TWIaddr
19	SPM_RDY	Flash-EPROM (geschrieben)	SPMRaddr

Tabelle 1: Interrupt-Quellen beim ATmega8

Nr.	Name	Bemerkung	Name in m16def.inc
1	RESET	Reset-Eingang, Einschalten	—
2	INT0	Externer Interrupt 0	INT0addr
3	INT1	Externer Interrupt 1	INT1addr
4	TIMER2_COMP	Timer 2	OC2addr
5	TIMER2_OVF	Timer 2	OVF2addr
6	TIMER1_CAPT	Timer 1	ICP1addr
7	TIMER1_COMPA	Timer 1	OC1Aaddr
8	TIMER1_COMPB	Timer 1	OC1Baddr
9	TIMER1_OVF	Timer 1	OVF1addr
10	TIMER0_OVF	Timer 0	OVF0addr
11	SPI_STC	SPI-Schnittstelle	SPIaddr
12	USART_RX	RS232-Schnittstelle	URXCaddr
13	USART_UDRE	RS232-Schnittstelle	UDREaddr
14	USART_TX	RS232-Schnittstelle	UTXCaddr
15	ADC	A/D-Umsetzer	ADCCaddr
16	EE_RDY	EEPROM	ERDYaddr
17	ANA_COMP	Komparator	ACIaddr
18	TWI	I ² C-Bus	TWIaddr
19	INT2	Externer Interrupt 2	INT2addr
20	TIMER0_COMP	Timer0	OC0addr
21	SPM_RDY	Flash-EPROM (geschrieben)	SPMRaddr

Tabelle 2: Interrupt-Quellen beim ATmega16

Nr.	Name	Bemerkung	Name in m32def.inc
1	RESET	Reset-Eingang, Einschalten	—
2	INT0	Externer Interrupt 0	INT0addr
3	INT1	Externer Interrupt 1	INT1addr
4	INT2	Externer Interrupt 2	INT2addr
5	TIMER2_COMP	Timer 2	OC2addr
6	TIMER2_OVF	Timer 2	OVF2addr
7	TIMER1_CAPT	Timer 1	ICP1addr
8	TIMER1_COMPA	Timer 1	OC1Aaddr
9	TIMER1_COMPB	Timer 1	OC1Baddr
10	TIMER1_OVF	Timer 1	OVF1addr
11	TIMER0_COMP	Timer 0	OC0addr
12	TIMER0_OVF	Timer 0	OVF0addr
13	SPI_STC	SPI-Schnittstelle	SPIaddr
14	USART_RX	RS232-Schnittstelle	URXCaddr
15	USART_UDRE	RS232-Schnittstelle	UDREaddr
16	USART_TX	RS232-Schnittstelle	UTXCaddr
17	ADC	A/D-Umsetzer	ADCCaddr
18	EE_RDY	EEPROM	ERDYaddr
19	ANA_COMP	Komparator	ACIaddr
20	TWI	I ² C-Bus	TWIaddr
21	SPM_RDY	Flash-EPROM (geschrieben)	SPMRaddr

Tabelle 3: Interrupt-Quellen beim ATmega32

Nr.	Name	Bemerkung	Name in m1284pdef.inc
1	RESET	Reset-Eingang, Einschalten	—
2	INT0	Externer Interrupt 0	INT0addr
3	INT1	Externer Interrupt 1	INT1addr
4	INT2	Externer Interrupt 2	INT2addr
5	PCI0	Pin-Change-Int. 0 bis 7	PCI0addr
6	PCI1	Pin-Change-Int. 8 bis 15	PCI1addr
7	PCI2	Pin-Change-Int. 16 bis 23	PCI2addr
8	PCI3	Pin-Change-Int. 24 bis 31	PCI3addr
9	WDT	Watchdog-Timer	WDTaddr
10	TIMER2_COMPA	Timer 2	OC2Aaddr
11	TIMER2_COMPB	Timer 2	OC2Baddr
12	TIMER2_OVF	Timer 2	OVF2addr
13	TIMER1_CAPT	Timer 1	ICP1addr
14	TIMER1_COMPA	Timer 1	OC1Aaddr
15	TIMER1_COMPB	Timer 1	OC1Baddr
16	TIMER1_OVF	Timer 1	OVF1addr
17	TIMER0_COMPA	Timer 0	OC0Aaddr
18	TIMER0_COMPB	Timer 0	OC0Baddr
19	TIMER0_OVF	Timer 0	OVF0addr
20	SPI_STC	SPI-Schnittstelle	SPIaddr
21	USART0_RX	RS232-Schnittstelle 0	URXC0addr
22	USART0_UDRE	RS232-Schnittstelle 0	UDRE0addr
23	USART0_TX	RS232-Schnittstelle 0	UTXC0addr
24	ANALOG_COMP	Komparator	ACIaddr
25	ADC	A/D-Umsetzer	ADCCaddr
26	EE_READY	EEPROM	ERDYaddr
27	TWI	I ² C-Bus	TWIaddr
28	SPM_READY	Flash-EPROM (geschrieben)	SPMRaddr
29	USART1_RX	RS232-Schnittstelle 1	URXC1addr
30	USART1_UDRE	RS232-Schnittstelle 1	UDRE1addr
31	USART1_TX	RS232-Schnittstelle 1	UTXC1addr
32	TIMER3_CAPT	Timer 3	ICP3addr
33	TIMER3_COMPA	Timer 3	OC3Aaddr
34	TIMER3_COMPB	Timer 3	OC3Baddr
35	TIMER3_OVF	Timer 3	OVF3addr

Tabelle 4: Interrupt-Quellen beim ATmega1284p